

FIG. 1

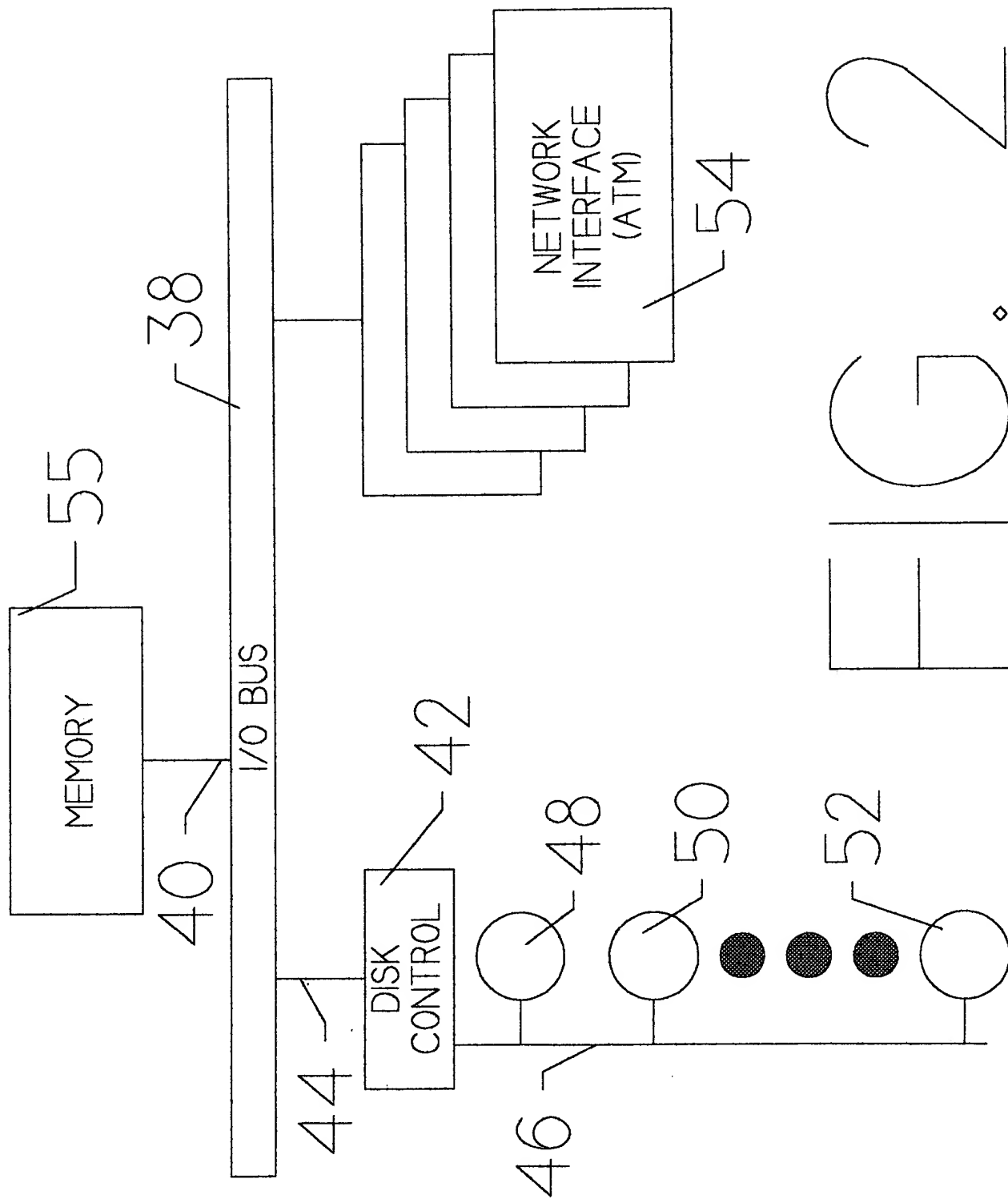


FIG. 2

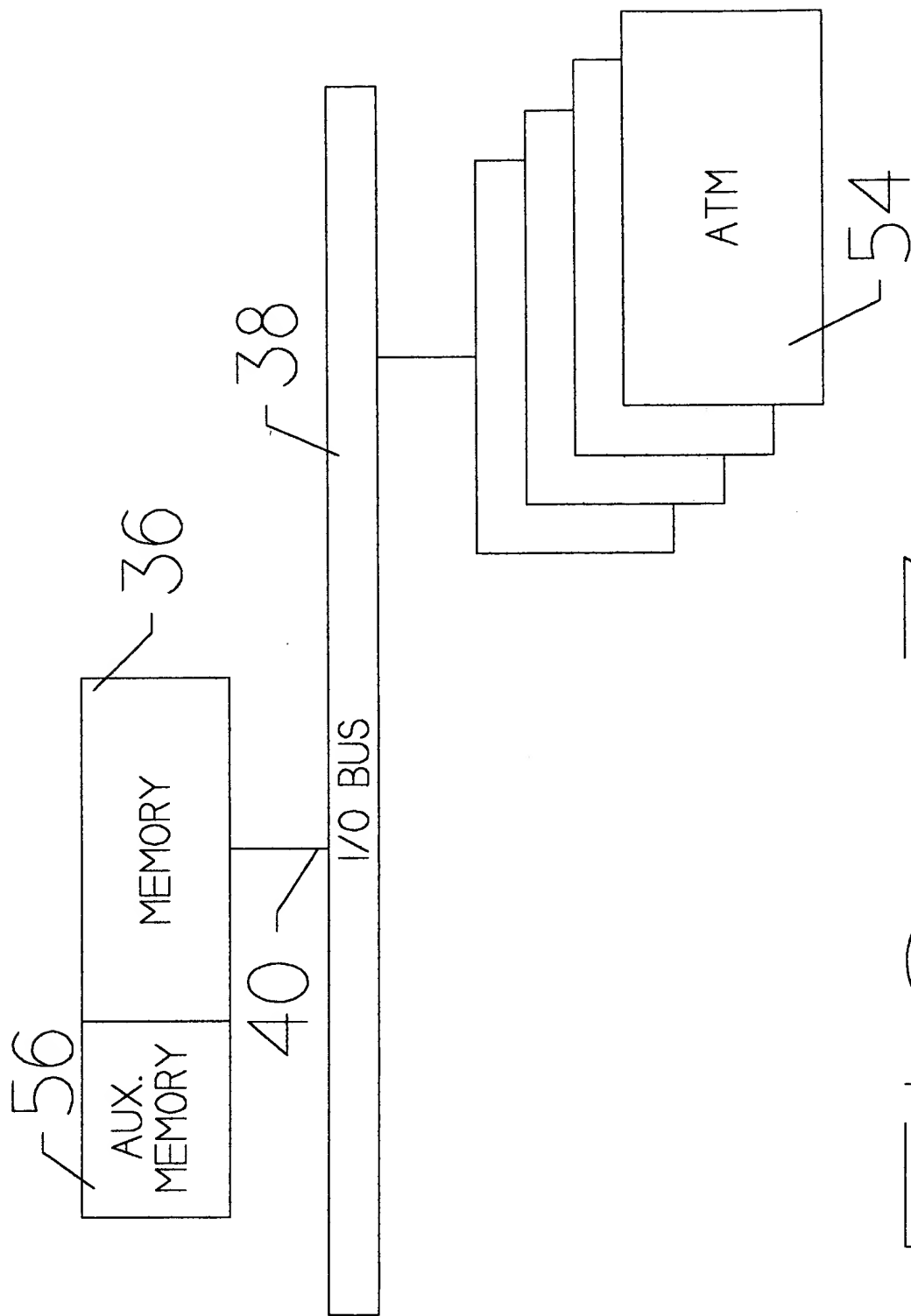


FIG. 3

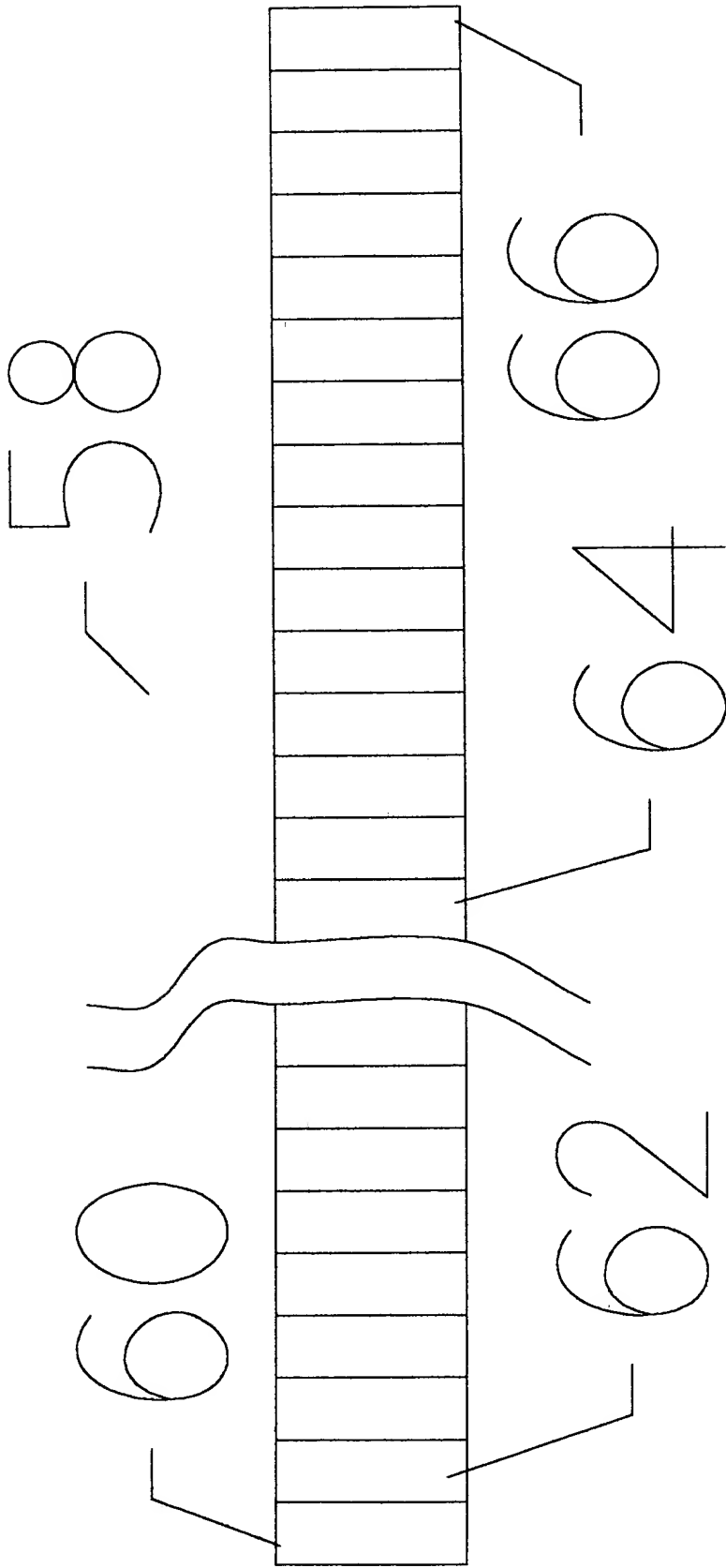


FIG. 4

FIG. 5 is a block diagram of a system 500. The system 500 includes a processor 510, a memory 520, a network interface 530, and a user interface 540. The processor 510 is connected to the memory 520, the network interface 530, and the user interface 540. The network interface 530 is connected to a network 550. The user interface 540 is connected to the processor 510.

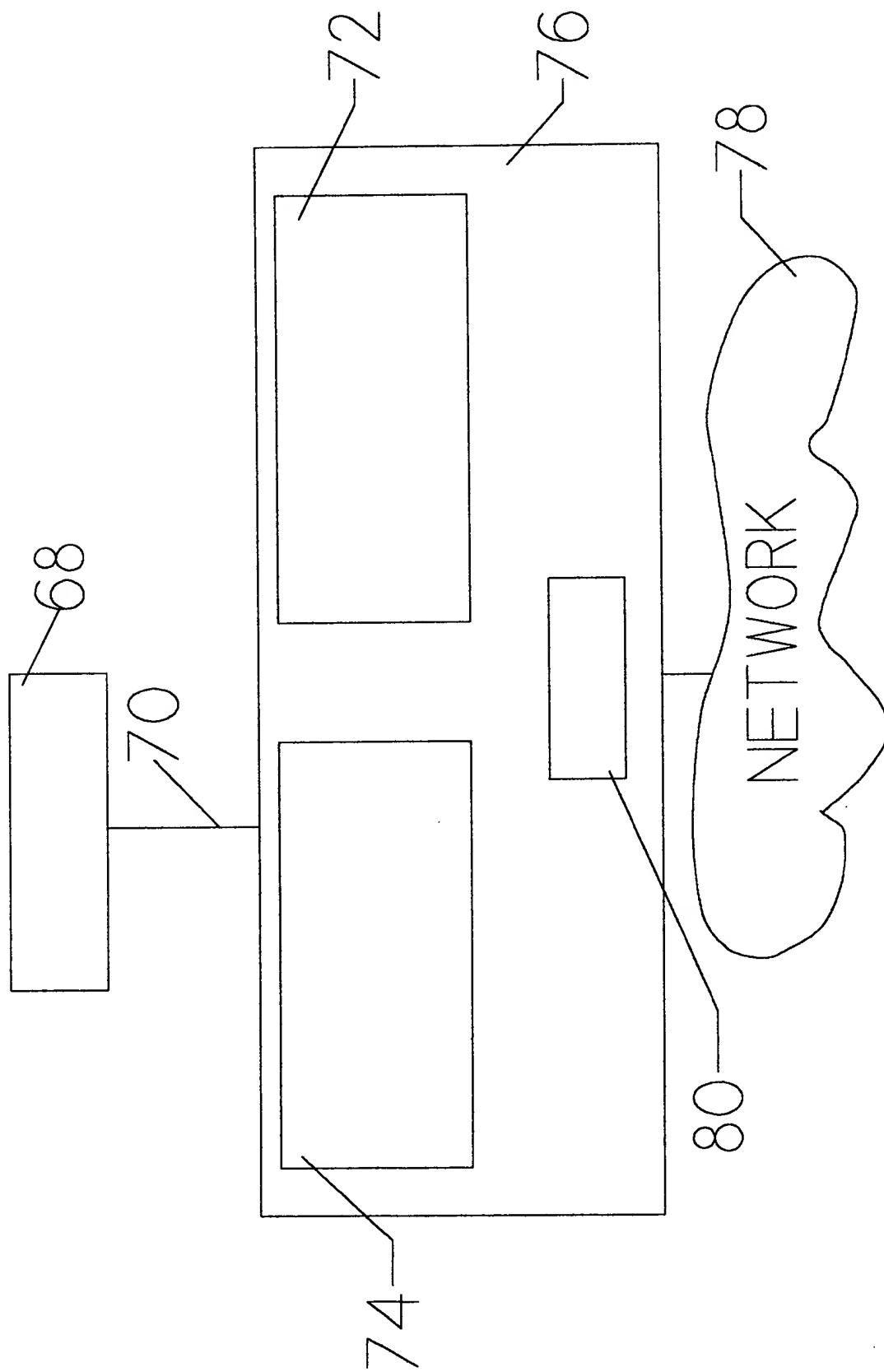


FIG. 5



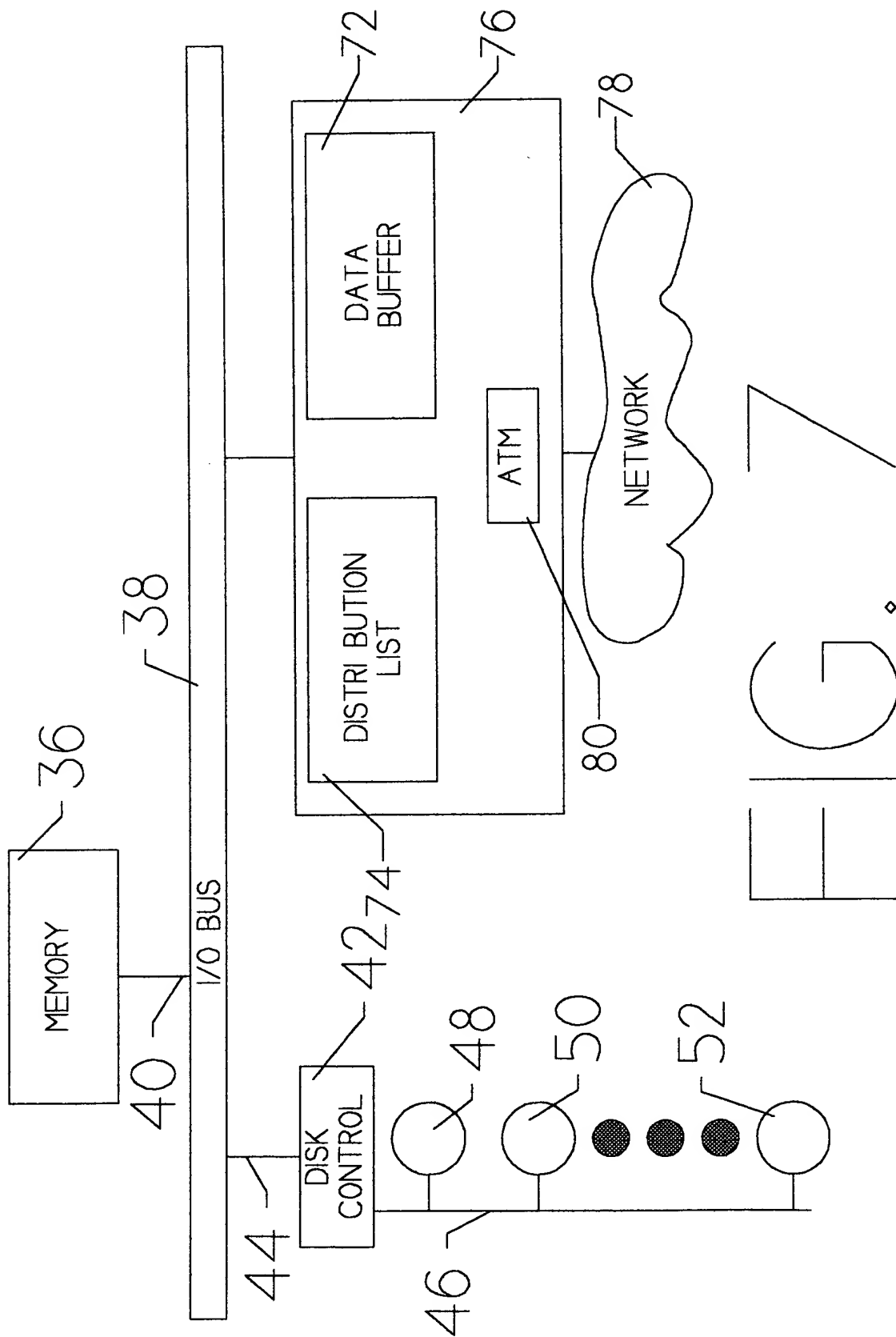


FIG. 7

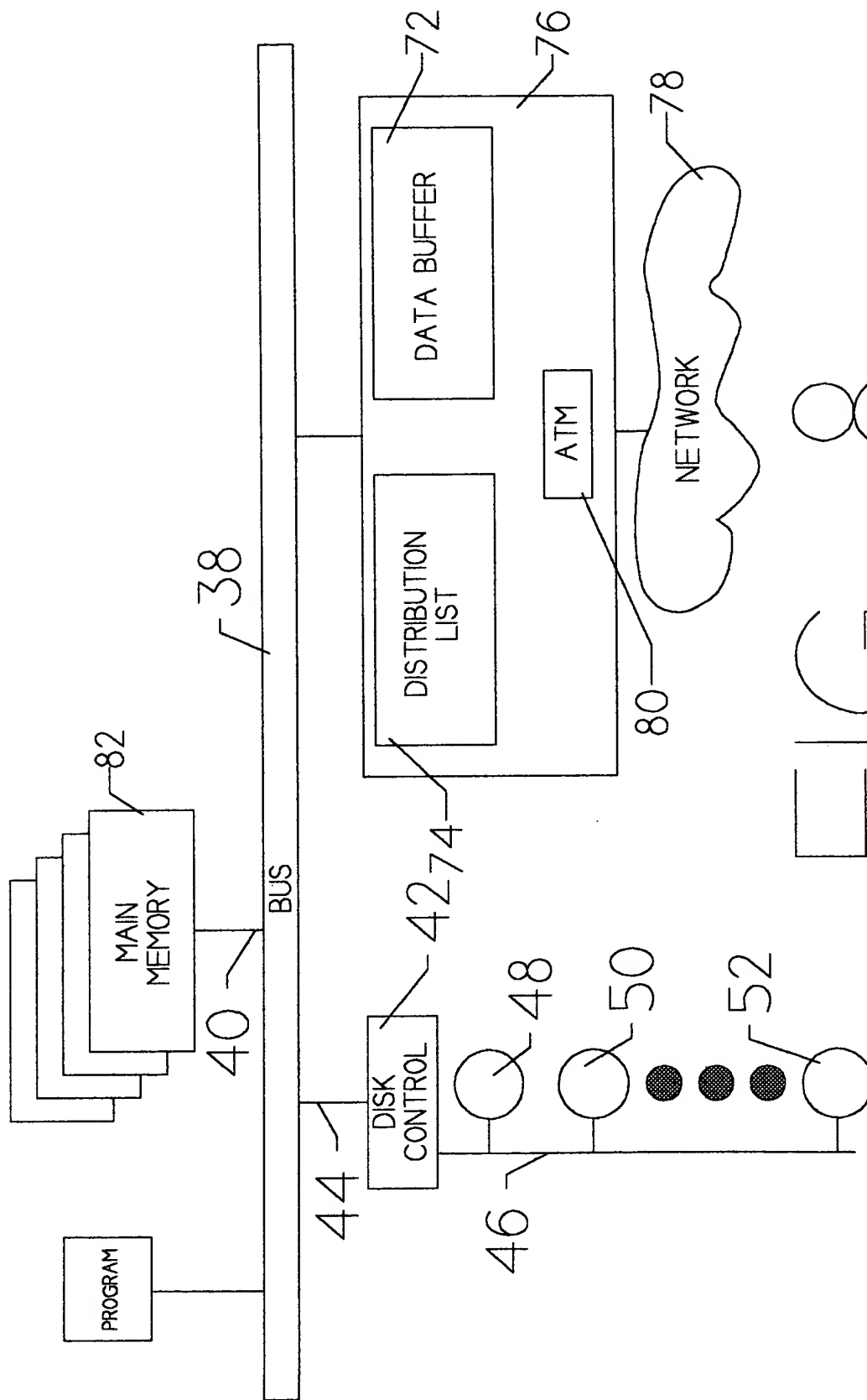


FIG. 8



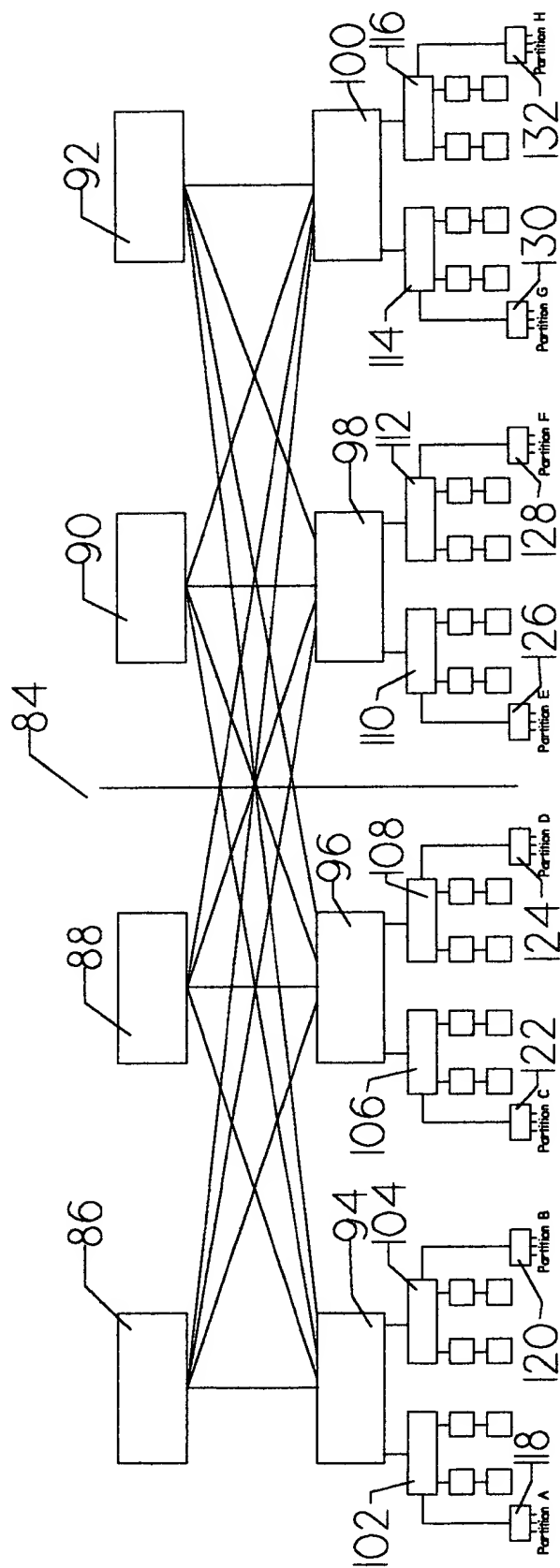


FIG. 9

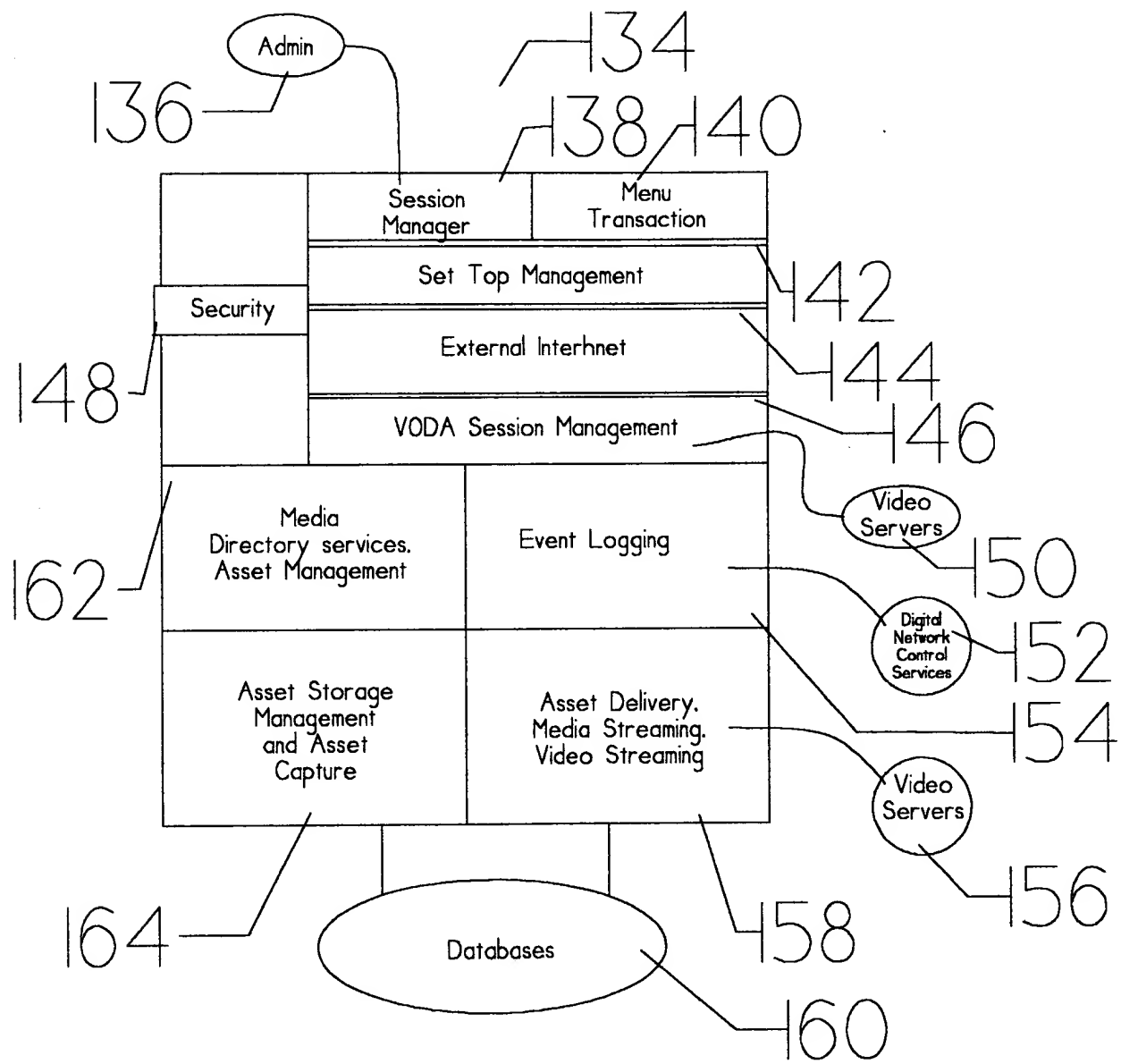


FIG. 10

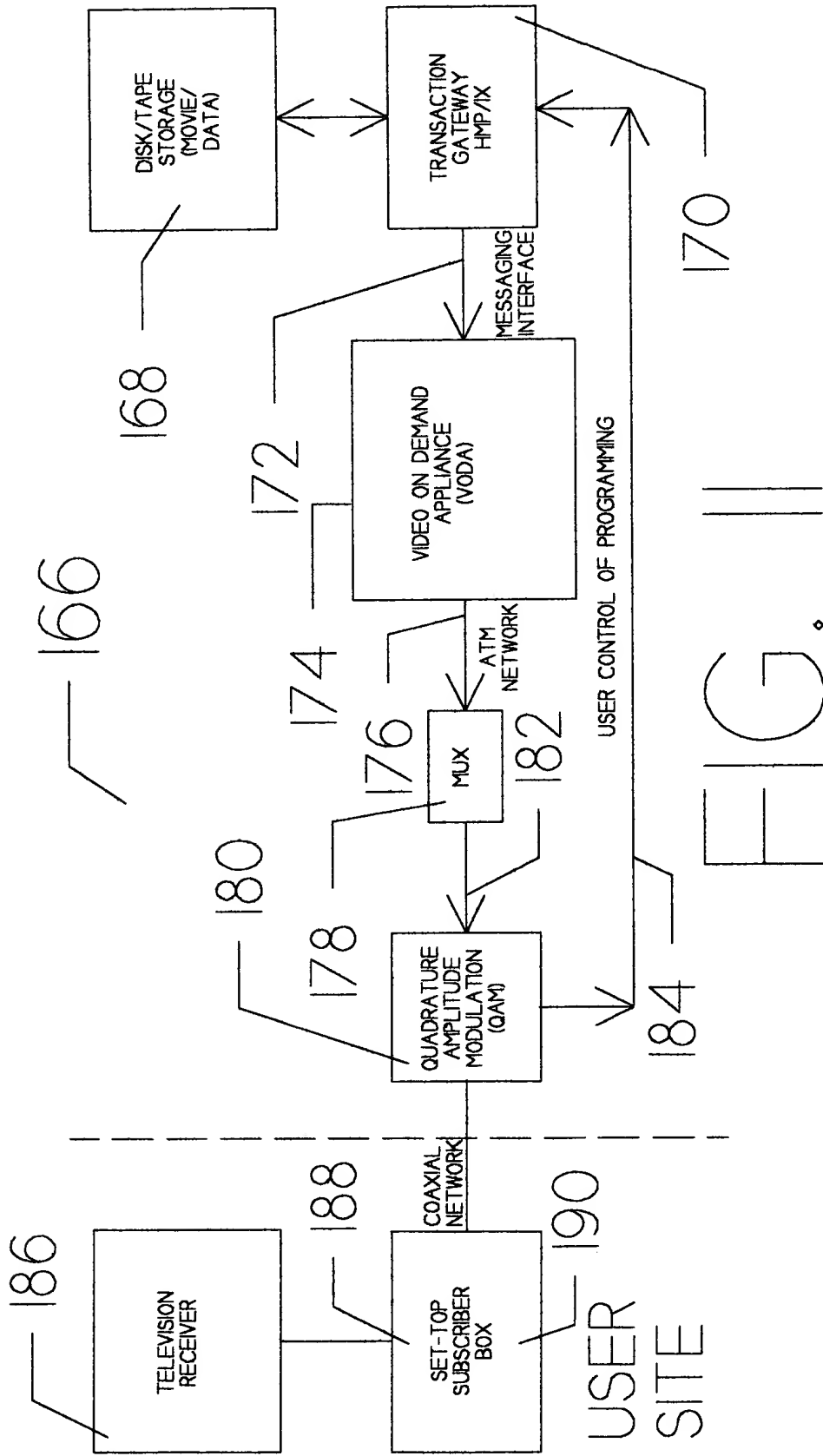


FIG. 1

